

S/N 09/852839

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	KONDO, et al.	Examiner:	V. Nguyen
Serial No.:	09/852839	Group Art Unit:	2858
Filed:	May 10, 2001	Docket No.:	10873.0713US01
Title:	SEMICONDUCTOR INTEGRATED CIRCUIT WITH NEGATIVE VOLTAGE GENERATION CIRCUIT, TEST METHOD FOR THE SAME, AND RECORDING DEVICE AND COMMUNICATION EQUIPMENT HAVING THE SAME		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on December 3, 2003.

By: Peggy Kerkhove
Name: Peggy KerkhoveAMENDMENT

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In connection with the above-identified application filed herewith, please enter the following amendment:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this

Remarks begin on page 10 of this paper.

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